Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S88	1	S87 and (fifo queu$3 pipeline pipe-line pip$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 10:39
S87	1	(US-6058466-$).did.	USPAT	OR	ON	2005/02/18 10:37
S86	19	((multi-processor-on-a-chip MP-on-a-chip (symmetric adj2 multithread$3) SMT (single adj2 physical adj3 processor adj3 chip) (one adj2 physical adj3 processor adj3 chip))) same ((logical adj2 (processor cpu)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/17 16:19
S85	7	((multi-processor-on-a-chip MP-on-a-chip (symmetric adj2 multithread$3) SMT (single adj2 physical adj3 processor adj3 chip) (one adj2 physical adj3 processor adj3 chip))) and ((logical adj2 (processor cpu)) with (busy unvailable non-empt$3 empt$3 idl$3 availabl$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/17 16:16
S83	3	(multi-processor-on-a-chip MP-on-a-chip (symmetric adj2 multithread$3) SMT (single adj2 physical adj3 processor adj3 chip) (one adj2 physical adj3 processor adj3 chip)) and (queu$3 pipeline pipe-line) and ((logical adj2 (processor cpu)) with (busy unvailable non-empt$3 empt$3 idl$3 availabl$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/17 16:16
S84	2	((multi-processor-on-a-chip MP-on-a-chip (symmetric adj2 multithread$3) SMT (single adj2 physical adj3 processor adj3 chip) (one adj2 physical adj3 processor adj3 chip)) same (queu$3 pipeline pipe-line)) and ((logical adj2 (processor cpu)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/17 16:04
S82	0	((multi-processor-on-a-chip MP-on-a-chip (symmetric adj2 multithread$3) SMT (single adj2 physical adj3 processor adj3 chip) (one adj2 physical adj3 processor adj3 chip)) same (queu$3 pipeline pipe-line)) and ((logical adj2 (processor cpu)) with (busy unvailable non-empt$3 empt$3 idl$3 availabl$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/17 16:04
S78	0	((multi-processor-on-a-chip MP-on-a-chip (single adj2 physical adj3 processor adj3 chip) (one adj2 physical adj3 processor adj3 chip)) same (queu$3 pipeline pipe-line)) and ((logical adj2 (processor cpu)) with (busy unvailable non-empt$3 empt$3 idl$3 availabl$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/17 16:02

S81	1	((multi-processor-on-a-chip MP-on-a-chip (single adj2 physical adj3 processor adj3 chip) (one adj2 physical adj3 processor adj3 chip))) and ((logical adj2 (processor cpu)) with (busy unvailable non-empt$3 empt$3 idl$3 availabl$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/17 16:00
S80	2	((multi-processor-on-a-chip MP-on-a-chip (single adj2 physical adj3 processor adj3 chip) (one adj2 physical adj3 processor adj3 chip))) and ((logical adj2 (processor cpu)))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/17 16:00
S79	0	((multi-processor-on-a-chip MP-on-a-chip (single adj2 physical adj3 processor adj3 chip) (one adj2 physical adj3 processor adj3 chip)) same (queu$3 pipeline pipe-line)) and ((logical adj2 (processor cpu)))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/17 15:59
S76	135	S75 and ((multiple plurality many two) near3 (queu$3 pipeline))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/17 15:52
S77	49	((vm (virtual adj2 machine) (virtual adj2 processor) (logical adj2 (processor cpu))) with (busy unvailable non-empt$3)) same ((vm (virtual adj2 machine) (virtual adj2 processor) (logical adj2 (processor cpu))) with (empt$3 idl$3 availabl$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/17 14:42
S74	49	((vm (virtual adj2 machine) (logical adj2 (processor cpu))) with (busy unvailable non-empt$3)) same ((vm (virtual adj2 machine) (logical adj2 (processor cpu))) with (empt$3 idl$3 availabl$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/17 14:41
S75	1570	(logical adj2 (processor cpu))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/17 14:38
S57	1566	(logical adj2 (processor cpu))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/17 14:37
S73	45	((vm (virtual adj2 machine)) with (busy unvailable non-empt$3)) same ((vm (virtual adj2 machine)) with (empt$3 idl$3 availabl$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/17 14:21
S71	91	((vm (virtual adj2 machine)) with (busy unvailable non-empt$3)) and ((vm (virtual adj2 machine)) with (empt$3 idl$3 availabl$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/17 14:21

S72	2	"6314511".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/17 11:19
S58	4	((logical adj2 (processor cpu)) with (busy unvailable non-empt$3)) and ((logical adj2 (processor cpu)) with (empt$3 idl$3 availabl$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/17 11:03
S59	98	((logical adj2 (processor cpu)) with (busy unvailable non-empt$3)) ((logical adj2 (processor cpu)) with (empt$3 idl$3 availabl$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/17 09:55
S37	68	((((logical) with (process$3 context)) and (load$3 near5 balanc$3) and ((queu$3 processor cpu) with (empt$3 bus$3 heav$3 light$3 idl$3) with (thread$3 task$3 job process processes))) and (((thread$3 task$3 job process processes resource) with manag$5) ((thread$3 task$3 job process processes) with (schedul$3 execut$3 run$4)) and (priorit$3 with schedul$3))) and ((context logical) with (swap$4 switch$3 chang$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/16 17:23